

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Marc Tremblay  
Title: GROUPING LOGIC CIRCUIT IN A PIPELINED SUPERSCALAR  
PROCESSOR  
Application No.: 09/583,097 Filed: August 2, 1999  
Examiner: David J. Huisman Group Art Unit: 2183  
Atty. Docket No.: 004-1391-1 Confirmation No.: 7166

August 6, 2003

Mail Stop Non Fee Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO OFFICE ACTION**

This paper is responsive to the Office action mailed March 6, 2003, having a shortened statutory period for response set to expire June 6, 2003. An accompanying Petition for Extension of Time extends the period for filing this response until August 6, 2003. In light of the Amendments and/or Remarks herein, further examination is requested.

08/11/2003 DWYATT 00000001 500631 09583097

01 FC:1252 410.00 DA

- 1 -

response to 3/6/03 non-final OA

Application No.: 09/583,097